

Docket No.: M4065.0468/P468 (PATENT)



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Application No.: 09/941,761

Group Art Unit: 2812

Filed: August 30, 2001

Examiner: Not Yet Assigned

For:

METHOD AND STRUCTURE FOR TEMPORARILY ISOLATING A DIE

FROM A COMMON CONDUCTOR TO FACILITATE WAFER LEVEL TESTING

INFORMATION DISCLOSURE STATEMENT (IDS)

Commissioner for Patents Washington, DC 20231

Dear Sir:

Pursuant to 37 CFR 1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits as far as is known to the undersigned.

A copy of each reference on PTO/SB/08 is attached.

The Commissioner is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0468/P468. A duplicate copy of this paper is enclosed.

Dated: January 7, 2002

Respectfully submitted,

Thomas J. D'Amico

Registration No.: 28,371

DICKSTEIN SHAPIRO MORIN &

OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicant